

Serial No. 10/511,760

Amendment Dated: February 2, 2006

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Amendments to the Specification:

Please replace the first paragraph on page 2 with the following amended paragraph:

Such a car navigation system includes an arithmetic processing unit constituted with a microprocessor and the like and a display device such as an LCD at which the road map, the recommended ~~reel~~ route and the like are displayed. In addition, an audio system, a television set achieved by using an LCD and the like may be installed in the vehicle.

Please replace the first paragraph on page 8 with the following amended paragraph:

The information processing apparatus A 1 having the AM tuner 22 and the FM tuner 24 is capable of receiving AM and FM radio broadcasts. The AM and FM radio broadcasts can be played in the vehicle by driving a speaker 41 ~~31~~ (see FIG. 4 3) with the audio amplifier 25. In addition, since it is equipped with an FM multiplex tuner, it is capable of receiving text messages and VICS information such as traffic information. The VICS is a road traffic information (vehicle information) communications system maintained and run by the Road Traffic Information Communications System Center (VICS Center, Vehicle Information Communication System Center) which is an incorporated foundation in Japan. If the information processing apparatus A 1 further includes a light

beacon receiving device and a radio wave beacon receiving device, VICS information transmitted in the light beacons and radio wave beacons, too, can be received as in FM multiplex broadcasts.

Please replace the second paragraph on page 9 and continuing on page 10 with the following amended paragraph:

FIG. 3 is a block diagram of the internal structure adopted in the display device 3. The display device 3 comprises an LCD panel 31, a backlight 32, a board 33 at which a control circuit is installed and the like. The LCD panel 31 may be, for instance, a 480 x 240 dot LCD or an 800 x 480 dot LCD. At the board 33, an arithmetic processing unit 34 constituted with a microprocessor and its peripheral circuits, a display control circuit 35 that controls the display at the LCD panel 31 based upon signals received from the arithmetic processing unit 34 31, a high-voltage source circuit 36 that drives the backlight 32 and the like are mounted. The arithmetic processing unit 34 controls the display at the LCD panel 31 and also executes other types of processing in response to instructions from the information processing apparatus A 1. Namely, the arithmetic processing unit 34 (Fig. 3) acts as a surrogate for the arithmetic processing unit 21 (Fig. 2) of the information processing apparatus A 1 by executing the other types of processing as well as the processing related to the display at the display device 3. This means that the display device 3 is intelligent.